



Docket No.: 95-528

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

SHEN *et al.*

Serial No.: 10/790,205

Filed: March 2, 2004

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Group Art Unit: (Not Assigned)

Examiner: (Not Assigned)

For: FAST FOURIER TRANSFORM CIRCUIT HAVING PARTITIONED MEMORY FOR
MINIMAL LATENCY DURING IN-PLACE COMPUTATION

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a Form PTO-1449 (PTO/SB/O8B) listing the enclosed documents. It is respectfully requested that the Examiner consider the enclosed documents in accordance with MPEP 609 by placing the Examiner's initials in the left column of the form PTO-1449, and returning the completed form PTO-1449 to the undersigned upon the next communication from the Patent Office.

This Information Disclosure Statement is being filed within three months of filing the application. No fee or certification is necessary.

This Information Disclosure Statement is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice that effect is earnestly solicited, along with additional time under Rule 97(f), to enable Applicant to comply fully.

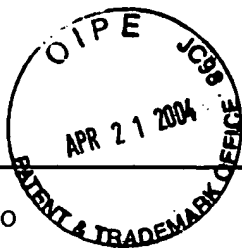
Respectfully submitted,

Manelli Denison & Selter PLLC

A handwritten signature in black ink, appearing to read 'L R Turkevich', written in a cursive style.

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Date: April 21, 2004



PTO/SB/08B (Modified)

Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Complete if Known		
			Application Number	10/790,205	
			Filing Date	March 2, 2004	
			First Named Inventor	Shen et al.	
			Group Art Unit	Not Assigned	
			Examiner Name	Not Assigned	
Sheet	1	of	1	Attorney Docket Number	95-528

OTHER PRIOR ART — NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		STEVEN W. SMITH, Ph.D., "The Fast Fourier Transform", <i>The Scientists and Engineer's Guide to Digital Signal Processing</i> , Chapter 12, from http://www.dspguide.com	
		LI et al, "Efficient Radix-4 and Radix-8 Butterfly Elements", NorChip99, Oslo, Norway, November 1999, http://www.es.isy.liu.se/publications/papers_and_reports/1999/weidongli_NorChip99.pdf	
		IEEE Std. 802.11a-1999, "Part 11: Wireless LAN Medium Access Control (MAC) and Physical Layer (PHY) specifications: High-speed Physical Layer in the 5 GHz Band", LAN/MAN Standards Committee of the IEEE Computer Society, IEEE-SA Standards Board, Approved September 16, 1999, New York, USA	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.